

Claims

1. A UVD circuit for monitoring a supply voltage and which includes:
a comparator for generating a shortfall signal indicative of a shortfall of
the supply voltage in relation to a reference voltage, and
5 an integrator for time-integrating the shortfall signal to form an
integrated signal,
wherein the output of the integrator is used to generate a reset signal.
2. A UVD circuit according to claim 1 further including a discriminator
10 circuit for receiving the integrated signal and at least one further output of the
comparator, and generating a reset signal using the integrated signal and the
at least one further output.
3. A UVD circuit according to claim 2 in which the discriminator circuit is
15 arranged to receive a control signal, the discriminator circuit further
comprising a switch controlled by the control signal for determining whether
the reset signal is generated based on the integrated signal or the at least one
further output signal.
- 20 4. A microprocessor incorporating a UVD circuit according to any
preceding claim, and reset means arranged to receive the reset signal output
by the UVD circuit and according to its value to initiate a reset of the
microprocessor.
- 25 5. A method of monitoring a supply voltage including:
generating a shortfall signal indicative of a shortfall of the supply
voltage in relation to a reference voltage;
time-integrating the shortfall signal to form an integrated signal; and
generating a reset signal using the shortfall signal.